In re application of: Harold E. Mattice et al.	Attorney Docket No.: IGT1P096/P000824-001
Application No.: 10/621,873	Examiner: Omkar A. Deodhar
Filed: July 16, 2003	Group: 3714
Title: SECURED VERIFICATION OF CONFIGURATION DATA FOR FIELD PROGRAMMABLE GATE ARRAY DEVICES	Confirmation No.: 1742

REQUEST FOR REFUND
(IMPROPER CHARGE OF DEPOSIT ACCOUNT)

ATTENTION: Refunds Section, Receipts Division, Office of Finance
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

I. REFUND REQUEST

This is a request for a refund with respect to the charge to Deposit Account 504480 shown on the monthly statement dated June 2010 (Seq. No. 1; Posting Ref Txt 10621873) for the above-identified patent application. The Patent Office erroneously charged Deposit Account 504480 for the addition of four dependent claims when Applicants did not add any dependent claims in the Amendment filed June 22, 2010, and all claims fees had been previously paid.

II. FEES CHARGED FOR WHICH REFUND REQUESTED

$208.00 (Seq. No. 1; Posting Ref Txt 10621873)

III. EXPLANATION OF WHY CONTESTED CHARGE IS IN ERROR

On June 22, 2010, an Amendment was filed in the above-referenced application (please see Exhibit A). In this submission, Applicants amended nine claims and **canceled four claims**. **No new claims were added** by way of this amendment. Applicants actually **decreased** the total number of claims in this amendment.

On June 24, 2010, the Patent Office charged Deposit Account 504480 for the addition for four dependent claims with respect to the Amendment filed on June 22, 2010 (please see Deposit Account Statement June 2010 and the Sales Receipt printed from PAIR, Seq. No. 1, Posting Ref Txt 10621873, attached as Exhibit B).
P.O. Box 70250
Oakland, CA 94612-0250
(510) 663-1100

Applicants have reviewed the entire claim history for the above-referenced application and submit that all claim fees have been previously paid. In order to expedite a refund, Applicants hereby list the claim history and payment history for all added claims. On July 16, 2003, the above-referenced application was filed with the USPTO and contained twenty dependent claims with four independent claims. On July 22, 2003, $84.00 was paid to cover the fee associated with the additional one independent claim (please see Exhibit C). On September 19, 2008, one dependent claim was added to the above-referenced application and a payment of $50.00 was charged to Deposit Account 504480 on that same date (please see Exhibit D). The claims now totaled twenty-one, with four independent claims, as evidenced by the "Patent Application Fee Determination Record" contained in Exhibit D. On September 15, 2009, Applicants added eight dependent claims and paid the $416.00 fee associated with this addition (8 @ $52.00 each; please see Exhibit E). **The claims now totaled twenty-nine, with four independent claims**, as evidenced by the "Patent Application Fee Determination Record" contained in Exhibit E.

The Patent Office improperly completed the "Patent Application Fee Determination Record" when determining the total claims previously paid (please see Exhibit F). On the form, the Patent Office lists twenty-one claims as the "Highest Number Previously Paid For". As outlined above, the highest number of previously paid for claims is twenty-nine (additionally, please see the "Patent Application Fee Determination Record" in Exhibit E, which was completed by the Patent Office on September 15, 2009, and clearly reflects a total of twenty-nine claims being paid).

Since Applicants had previously paid for a total of twenty-nine claims, where four of those claims were independent, and the Amendment filed on June 22, 2010 contained a total of twenty-five claims, where four of those claims were independent, **no additional claim fees should have been charged** to Deposit Account 504480. **As such, we believe our Deposit Account 504480 was charged in error and request a refund in the amount of $208.00 to the same deposit account**.

IV. MANNER OF REFUND

Please make refund by crediting Deposit Account 504480 (Order No. IGT1P096/ P000824-001).

Respectfully submitted,
Weaver Austin Villeneuve & Sampson LLP

/Reginald J. Suyat/
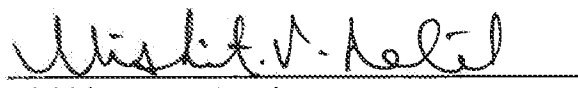Reginald J. Suyat
Registration No. 28,172

Nishitkumar V. Patel
Registration No. 65,546

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EXHIBIT A

Electronic Acknowledgement Receipt

EFS ID:	7863104
Application Number:	10621873
International Application Number:	
Confirmation Number:	1742
Title of Invention:	Secured verification of configuration data for field programmable gate array devices
First Named Inventor/Applicant Name:	Harold E. Mattice
Customer Number:	79646
Filer:	Reginald J. Suyat/Nish Patel/ Michelle Heymann
Filer Authorized By:	Reginald J. Suyat
Attorney Docket Number:	IGT1P096/P000824-001
Receipt Date:	22-JUN-2010
Filing Date:	16-JUL-2003
Time Stamp:	14:50:34
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	$310
RAM confirmation Number	1160
Deposit Account	504480
Authorized User	
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	
Charge any Additional Fees required under 37 C.F.R. Section 1.21 (Miscellaneous fees and charges)	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Transmittal Letter	IGT1P096_Amendment_G_Transmittal.pdf	63478 / be18f7546e548c7c1d6f19a96c68a2ca9ac44e05	no	1

Warnings:

Information:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
2		IGT1P096_Amendment_G.pdf	735437 / 8c7f1931e22a4ecac3c4f034da5f3601d7a935fd	yes	14

Multipart Description/PDF files in .zip description			
Document Description	Start		End
Amendment/Req. Reconsideration-After Non-Final Reject	1		1
Claims	2		10
Applicant Arguments/Remarks Made in an Amendment	11		14

Warnings:

Information:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
3	Information Disclosure Statement (IDS) Filed (SB/08)	IGT1P096_1449.pdf	63932 / 865974cd3a01f26f703be3c5e41f99a973f5eddc	no	1

Warnings:

Information:

This is not an USPTO supplied IDS fillable form

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
4	Foreign Reference	1_GB2404757.pdf	2074529 / 1310357512ca88280b52ce44d01b7365e6efbef0	no	58

Warnings:

Information:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
5	NPL Documents	2_IGT1P096GB_Exam_Report_101805.pdf	210902 / 09d179e1c6b4f6d346c5f1640723177a82445895	no	3

Warnings:

Information:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
6	Transmittal Letter	IGT1P096_IDS_Transmittal.pdf	141831 / 3a4d059c880fe097817b6b9e6a3867b7e23a2464	no	2

Warnings:

Information:

| 7 | Fee Worksheet (PTO-875) | fee-info.pdf | 31993 | no | 2 |
| | | | 3db921e60d2e0bc8f9e9c410c582645e666bc39d | | |

Warnings:

Information:

| | | | | Total Files Size (in bytes): | 3322102 |

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

Electronic Patent Application Fee Transmittal

Application Number:	10621873
Filing Date:	16-Jul-2003
Title of Invention:	Secured verification of configuration data for field programmable gate array devices
First Named Inventor/Applicant Name:	Harold E. Mattice
Filer:	Reginald J. Suyat/Nish Patel/ Michelle Heymann
Attorney Docket Number:	IGT1P096/P000824-001

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 1 month with $0 paid	1251	1	130	130

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Miscellaneous:				
Submission- Information Disclosure Stmt	1806	1	180	180
Total in USD ($)				**310**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Richard L. Wilder et al.	Attorney Docket No.: IGT1P096/P000824-001
Application No.: 10/621,873	Examiner: Omkar A. Deodhar
Filed: July 16, 2003	Group: 3714
Title: SECURED VERIFICATION OF CONFIGURATION DATA FOR FIELD PROGRAMMABLE GATE ARRAY DEVICES	Confirmation No.: 1742

AMENDMENT G TRANSMITTAL

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith is an Amendment in the above-identified application.

The fee has been calculated as shown below.

	Claims After Amendment		Highest Previously Paid For	Present Extra	Small Entity Rate Fee	Large Entity Rate Fee
Total Claims	25	MINUS	29	1	x 25 =	x 52 = $ 0.00
Independent Claims	4	MINUS	4	0	x 105 =	x 210 = $ 0.00
One-month Extension of Time						$130.00
				Total	$	$130.00

☒   Applicant(s) hereby petitions for a one-month extension(s) of time to respond to the aforementioned Office Action.

☒   Applicant(s) believe that no additional Extension of Time is required; however, if it is determined that such an extension is required, Applicant(s) hereby petition that such an extension be granted and authorize the Commissioner to charge the required fees for an Extension of Time under 37 C.F.R. §1.136 to Deposit Account No. 504480.

☐   Enclosed is our Check No.   in the amount of $ to cover the additional claim fee and/or extension of time fees.

☒   Please charge the required fees, or any additional fees required to facilitate filing the enclosed response, to Deposit Account No. 504480 (Order No. IGT1P096/P000824-001).

Respectfully submitted,
Weaver Austin Villeneuve & Sampson LLP
/Reginald J. Suyat/
Reginald J. Suyat
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Harold E. Mattice et al.	Attorney Docket No.: IGT1P096/P000824-001
Application No.: 10/621,873	Examiner: Omkar A. Deodhar
Filed: July 16, 2003	Group: 3714
Title: SECURED VERIFICATION OF CONFIGURATION DATA FOR FIELD PROGRAMMABLE GATE ARRAY DEVICES	Confirmation No.: 1742

AMENDMENT G

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action of February 22, 2010, please amend the above-identified patent application and consider the following remarks.

Claims are reflected in the Listing of Claims, which begins on page 2 of this paper. This Listing of Claims replace all prior versions and listings of claims in this application.

Remarks begin on page 11 of this paper.

LISTING OF CLAIMS:

1. (Currently amended) A method of authenticating configuration data within or about a gaming machine with respect to a gaming machine boot process, the method comprising:

configuring a central processing unit to execute executable programming instructions to generate a wager-based game on the gaming machine;

storing the executable programming instructions in a memory device for generating the wager-based game;

connecting a volatile programmable electronic device comprising a plurality of logic elements programmable to form logic gates in a communication path between the central processing unit and the memory device;

monitoring a communication between the central processing unit and at least one of an input device and an output device by using the volatile programmable electronic device;

storing instructions for configuring the volatile programmable electronic device to enable communications between the central processing unit and the memory device, wherein said storing instructions comprises storing the instructions within a read only configuration file included within a configurator;

accessing a separate read only custodial file, wherein at least a substantial portion of said custodial file is identical to at least a substantial portion of said configuration file when said configuration file is authentic, said custodial file residing in a location separate from said configurator;

determining whether to hold said volatile programmable electronic device in a reset mode;

if the reset mode is held, holding [[the]] a plurality of operating contents of said volatile programmable electronic device as substantially empty upon a shut down phase of said gaming machine to disable communication between the central processing unit and the memory device;

~~determining whether to hold said volatile programmable electronic device in a reset mode;~~

booting up said gaming machine after said shut down phase;

transferring said configuration file from said configurator to said volatile programmable electronic device;

~~configuring said volatile programmable electronic device with said configuration file;~~

comparing at least a representative portion of data from said configuration file with at least a representative portion of data from said custodial file;

confirming whether said configuration file has been successfully compared to said custodial file;

continuing to hold said volatile programmable electronic device in the reset mode until said confirming of said successful comparison is completed;

configuring said volatile programmable electronic device with said configuration file;

permitting a substantial amount of regular gaming machine operations only after ~~a~~ ~~successful~~ confirming [[step]] **that said configuration file has been successfully compared to said custodial file**; and

facilitating communication between said memory device and said central processing unit upon ~~determining~~ **said confirming** that said configuration file has been successfully compared to said custodial file.

2.   (Previously presented) The method of claim 1, wherein said storing the instructions within a read only configuration file included within a configurator comprises storing the instructions within a read only configuration file included within the configurator that comprises a memory unit.

3.   (Previously presented) The method of claim 2, wherein said storing the instructions within a read only configuration file included within a configurator comprises storing the instructions within a read only configuration file included within the configurator having a memory unit that comprises a standard Read Only Memory.

4.   (Previously presented) The method of claim 2, wherein said storing the instructions within a read only configuration file included within a configurator comprises storing the instructions within a read only configuration file included within the configurator having a memory unit that comprises an Electrical Erasable Programmable Read Only Memory.

5.   (Previously presented) The method of claim 1, wherein said connecting a volatile programmable electronic device comprises connecting a Field Programmable Gate Array in the communication path between the central processing unit and the memory device.

6.   (Previously presented) The method of claim 1, wherein said connecting a volatile programmable electronic device comprises connecting a Simple Programmable Logic Device or a Complex Programmable Logic Device in the communication path between the central processing unit and the memory device.

7.   (Original) The method of claim 1, wherein said central processing unit, said volatile programmable electronic device and said configurator all reside within the gaming machine.

8.   (Previously presented) The method of claim 1, wherein said comparing at least a representative portion is performed by said central processing unit.

9.   (Original) The method of claim 8, wherein said custodial file is located within said central processing unit.

10.   (Canceled)

11.   (Currently amended) The method of claim [[10]] 1, wherein said confirming step is performed prior to said transferring step.

12.   (Original) The method of claim 1, wherein said configurator is located within said central processing unit.

13.   (Currently amended) A microprocessor based gaming machine, comprising:
      a central processing unit designed or configured to execute executable programming instructions used to generate a wager-based game on the microprocessor based gaming machine;
      at least one of an input device and an output device;

a memory device used in conjunction with the microprocessor based gaming machine, said memory device configured to store the executable programming instructions for generating the wager-based game;

a volatile programmable electronic device, said volatile programmable electronic device comprising a plurality of logic elements programmable to form logic gates, said volatile programmable volatile programmable electronic device disposed in a communication path between the central processing unit and the memory device, said volatile programmable electronic device configured to monitor a communication between the central processing unit and said at least one of said input device and said output device, wherein said central processing unit is configured to determine whether to hold said volatile programmable electronic device in a reset mode;

a configurator;

a read only configuration file located within said configurator and adapted to be used in configuring said volatile programmable electronic device, said read only configuration file comprising instructions for configuring the volatile programmable electronic device to enable communications between the central processing unit and the memory device;

a separate custodial file located within the microprocessor based gaming machine and separate from said configurator, wherein at least a substantial portion of said separate custodial file is identical to at least a substantial portion of said configuration file; and

a comparator designed to compare at least a representative portion of data from said configuration file with at least a representative portion of data from said custodial file ~~in order~~ to authenticate said configuration file, said comparator adapted to provide a signal to said central processing unit regarding the results of said comparison, **said central processing unit configured to continue to determine whether to hold said volatile programmable electronic device in the reset mode until the authentication is completed,** said volatile programmable electronic device configured to facilitate communication between said memory device and said central processing unit based on the results of said comparison.

14.   (Original) The microprocessor based gaming machine of claim 13, wherein said volatile programmable electronic device comprises a Field Programmable Gate Array.

15.   (Original) The microprocessor based gaming machine of claim 13, wherein said configurator comprises an Electrical Erasable Programmable Read Only Memory.

16.   (Original) The microprocessor based gaming machine of claim 13, wherein said comparator is located within said central processing unit.

17.   (Original) The microprocessor based gaming machine of claim 13, wherein said custodial file is located within said central processing unit.

18.   (Original) The microprocessor based gaming machine of claim 13, wherein said configurator is located within said central processing unit.

19.   (Currently amended) A method of authenticating configuration data in a microprocessor based machine during a machine boot process, comprising:

    configuring a central processing unit to execute executable programming instructions for generating a wager-based game on the microprocessor based machine;

    storing the executable programming instructions in a memory device to generate the wager-based game;

    determining whether to hold said primary volatile programmable electronic device in a reset mode;

    holding [[the]] a plurality of operating contents of a primary volatile programmable electronic device associated with the microprocessor based machine as substantially empty upon a shut down phase of the microprocessor based machine, wherein said holding the operating contents comprises holding the operating contents of the primary volatile programmable electronic device including a plurality of logic elements programmable to form logic gates;

    disposing the primary volatile programmable electronic device in a communication path between the central processing unit and the memory device;

    monitoring a communication between the central processing unit and at least one of an input device and an output device by using the primary volatile programmable electronic device;

disabling communication between the central processing unit and the memory device by performing said holding of the operating contents of the primary volatile programmable electronic device;

~~determining whether to hold said primary volatile programmable electronic device in a reset mode;~~

booting up the microprocessor based machine after said shut down phase;

transferring a read only configuration file to said volatile programmable electronic device, said read only configuration file comprising instructions for configuring the volatile programmable electronic device to enable communications between the central processing unit and the memory device;

~~configuring said volatile programmable electronic device with said configuration file;~~

comparing at least a representative portion of data from said configuration file with at least a representative portion of data from a separate custodial file,

wherein at least a substantial portion of said separate custodial file is identical to at least a substantial portion of said configuration file,

and wherein said separate custodial file resides in a location separate from said memory device;

confirming whether said configuration file has been successfully compared to said custodial file;

determining to continue said holding of said volatile programmable electronic device in the reset mode until said confirming of said successful comparison is completed;

configuring said volatile programmable electronic device with said configuration file;

permitting a substantial amount of regular microprocessor based machine operations only after ~~a successful~~ said confirming [[step]] **that said configuration file has been successfully compared to said custodial file**; and

facilitating communication between said memory device and said central processing unit upon ~~determining~~ **said confirming** that said configuration file has been successfully compared to said custodial file.

20. (Currently amended) A method of authenticating data in a microprocessor based machine, comprising:

configuring a central processing unit (CPU) within the microprocessor based machine to execute executable programming instructions for generating a wager-based game on the microprocessor based machine;

storing the executable programming instructions in a memory device within the microprocessor based machine to generate the wager-based game;

disposing a field programmable gate array (FPGA) in a communication path between the ~~central processing unit~~ **CPU** and the memory device, said FPGA located within the microprocessor based machine, and said FPGA comprising a plurality of logic elements programmable to form logic gates;

monitoring a communication between the ~~central processing unit~~ **CPU** and at least one of an input device and an output device by using the FPGA;

storing a configuration file within a configurating EEPROM located within the microprocessor based machine, said configuration file comprising instructions for configuring the FPGA to enable communications between the ~~central processing unit~~ **CPU** and the memory device;

storing a separate custodial file within the microprocessor based machine and separate from said EEPROM, wherein at least a substantial portion of said separate custodial file is identical to at least a substantial portion of said configuration file;

determining whether to hold said FPGA in a reset mode;

holding a plurality of operating contents of said FPGA as substantially empty upon a shut down phase of the microprocessor based machine to disable communication between the ~~central processing unit~~ **CPU** and the memory device;

~~determining whether to hold said FPGA in a reset mode;~~

booting up the microprocessor based machine;

initiating a request to transfer said configuration file from said EEPROM to said FPGA;

utilizing said CPU to compare at least a representative portion of data from said configuration file with at least a representative portion of data from a separate custodial file;

confirming whether said configuration file has been successfully compared to said custodial file satisfaction;

determining to continue said holding of said FPGA in the reset mode until said
confirming of said successful comparison is completed;

configuring said FPGA with said configuration file; and

facilitating communication between said memory device and said CPU upon
~~determining~~ **said confirming** that said configuration file has been successfully compared to said
custodial file.

21.   (Previously Presented) The method of claim 1, further comprising determining not to
facilitate communication between said memory device and said central processing unit upon
determining that said configuration file has been unsuccessfully compared to said custodial file.

22.   (Previously presented) The microprocessor based gaming machine of claim 13, wherein
said input device comprises a coin in switch or an input switch, and said output device comprises
a video display or a speaker.

23.   (Canceled)

24.   (Currently amended) The method of ~~claim 23~~ **claim 1**, further comprising taking said
volatile programmable electronic device out of the reset mode upon ~~determining~~ **said**
confirming that said configuration file is successfully compared to said custodial file.

25.   (Canceled)

26.   (Currently amended) The microprocessor based gaming machine of ~~claim 25~~ **claim 13**,
wherein said central processing unit is configured to take said volatile programmable electronic
device out of the reset mode upon determining that **the authentication including a successful**
comparison of said configuration file ~~is successfully compared~~ to said custodial file **is**
completed.

27.   (Canceled)

28.   (Currently amended) The method of ~~claim 27~~ claim 19, further comprising taking said volatile programmable electronic device out of the reset mode upon ~~determining~~ said confirming that said configuration file is successfully compared to said custodial file.

29.   (Canceled)

30.   (Currently amended) The method of ~~claim 29~~ claim 20, further comprising taking said FPGA out of the reset mode upon ~~determining~~ said confirming that said configuration file is successfully compared to said custodial file.

REMARKS

The Office Action of January 22, 2010 has been carefully considered by Applicants. Applicants may desire an opportunity to conduct one or more Examiner interviews to further prosecution of this case. Claims 1-9 and 11-30 have been rejected. Claims 1, 11, 13, 19, 20, 24, 26, 28, and 30 are amended. Claims 23, 25, 27, and 29 are canceled. No new matter has been added.

Claim Rejection under 35 U.S.C. § 112

Claim 1 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicants regard as their invention. Applicants have amended claim 1 to overcome this rejection. Accordingly, Applicants respectfully request that the Section 112 rejection be withdrawn.

Claim Rejection under 35 U.S.C. § 103(a)

Claims 1-9 and 11-30 are rejected under 35 U.S.C. 103(a) as being unpatentable over Alcorn (U.S. Patent No. 6,149,522).

Applicants believe the rejection is deficient for the following reasons.

Applicants respectfully submit that a prima facie case of obviousness is not made. A reason as to why the recitation of "determining whether to hold said volatile programmable electronic device in a reset mode" would have been obvious over the cited art is not stated in the Office Action. The following is stated on pages 7 and 8 of the Office Action:

Regarding the amendment, "determining whether to hold said volatile programmable electronic device in a reset mode," while Applicant's Specification, Page 29, Paragraph 56, discloses that the FPGA can be held in reset mode, the Specification goes on to disclose that in other embodiments, "holding of the FPGA in reset mode is presumable not possible, while in other embodiments, such a hold may be optional, or designed as mandatory, as desired." This is viewed as admission that placing the volatile device in reset mode is merely a design consideration.

Therefore, since Applicant has disclosed that holding the volatile device in reset
mode is optional, Examiner cannot but conclude that this have been a matter of obvious
design choice to a person of ordinary skill in the art at the time of Applicant's invention.

Thus, it is initially indicated in the Office Action that holding an FPGA in a reset mode is
a design consideration (see the conclusion of the Office Action's first paragraph quoted above).
It is then stated in the Office Action that "since Applicants have disclosed that holding the
volatile device in reset mode is optional, Examiner cannot but conclude that this have been a
matter of *obvious* design choice" (Emphasis added). Applicants disagree. The feature is one of
the different embodiments. Applicants respectfully disagree with the Examiner's
characterization, and submit that the Examiner has not presented that the feature is obvious in the
context of all elements of the claim.

Applicants respectfully submit that no reason is provided as to why the feature of
"determining whether to hold said volatile programmable electronic device in a reset mode" is
obvious. This feature is supported on page 9, paragraph 56 of the specification. Hence,
Applicants respectfully submit that a prima facie case of obviousness is not made and
respectfully request that the Examiner provide a reason as to why the feature of "determining
whether to hold said volatile programmable electronic device in a reset mode" would be obvious.

The Examiner further states on page 10 of the Office Action:

Claims 23-30: These claims require determining when the FPGA is placed in
reset mode & when it is removed from the reset mode. As explained above with respect
to claim 1, however, since Applicant disclosed that holding the FPGA in reset mode is
optional, (See Specification, Page 29, Paragraph 56), these limitations would have been
matters of obvious design choice to a person of ordinary skill in the art at the time of
Applicant's invention.

Again, no reason is provided as to why claims 23-30 would have been obvious over the
cited art. Rather, the specification is cited to determine that claims 23-30 would have been
obvious. Applicants own specification cannot be used to establish a prima case of obviousness
because the Applicants specification is not prior art. Thus, Applicants respectfully submit that a
prima facie case of obviousness is not made with respect to claims 23-30.

Moreover, claims as amended describe limitations, such as recited in claim 1, "continuing to hold said volatile programmable electronic device in the reset mode until said confirming of said successful comparison is completed". Applicants believe the citations providing by the Examiner don't teach or suggest a microprocessor based machine, such as a gaming machine, in the manner as described in the pending claims.

As described in the claims, a volatile programmable electronic device, such as a field programmable gate array, is disposed in a communication path between a CPU and a memory device storing the executable programming instructions for generating the wager-based game (see FIG. 3 of the pending application). The volatile programmable electronic device comprises a plurality of logic elements to form logic gates. The volatile programmable electronic device may be programmed to enable communications between the CPU and the memory device storing executable programming instructions for generating the wager-based game. Further, the operating contents of the volatile programmable electronic device may be held as substantially empty upon a shut down phase of the gaming machine to disable communication between the central processing unit and the memory device. In addition, the volatile programmable electronic device is configured to monitor communication between the central processing unit and at least one of an input device and an output device. Further, there is a comparison of at least a representative portion of data from a configuration file with at least a representative portion of data from a custodial file. There is also a confirmation of whether the configuration file has been successfully compared to the custodial file. The volatile programmable electronic device is continuously held in the reset mode until the confirmation of the successful comparison is completed. Applicants believe that the prior art citations provided by the Examiner do not teach or suggest a gaming machine configured with a volatile programmable memory device that is used in this manner.
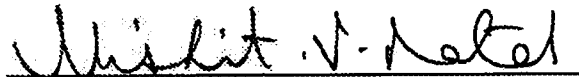
CONCLUSION

Applicants believe that all pending claims are allowable and respectfully requests a Notice of Allowance for this application from the Examiner. Should the Examiner believe that a telephone conference would expedite the prosecution of this application, the undersigned can be reached at the telephone number set out below.

The Commissioner is hereby authorized to charge any additional fees, including any extension fees, which may be required or credit any overpayment directly to the account of the undersigned, No. 504480 (Order No. IGT1P096/P000824-001).

Respectfully submitted,
Weaver Austin Villeneuve & Sampson LLP
/Reginald J. Suyat/

Reginald J. Suyat
Registration No. 28,172

Respectfully submitted,

Nishitkumar V. Patel
Reg. No. 65,546
P.O. Box 70250
Oakland, CA 94612-0250
(510) 663-1100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Richard L. Wilder et al.	Attorney Docket No.: IGT1P096/P000824-001
Application No.: 10/621,873	Examiner: Omkar A. Deodhar
Filed: July 16, 2003	Group: 3714
Title: SECURED VERIFICATION OF CONFIGURATION DATA FOR FIELD PROGRAMMABLE GATE ARRAY DEVICES	Confirmation No.: 1742

INFORMATION DISCLOSURE STATEMENT
BEFORE FINAL ACTION OR NOTICE OF ALLOWANCE
(37 CFR §§ 1.56 AND 1.97(c))

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The reference(s) identified in the attached PTO Form 1449, (copies of non-U.S. references are attached), may be material to examination of the above-identified patent application. Applicants identify the reference(s) in compliance with their duty of disclosure pursuant to 37 C.F.R. §§1.56 and 1.97. The Examiner is requested to make the identified reference(s) of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that the reference(s) indeed constitutes prior art.

This Information Disclosure Statement is being filed after the mailing date of the first Office Action on the merits, or after three months from the filing date of this application, whichever event
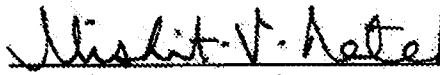
occurred last, but it is believed before the mailing date of either: (i) a final action under §1.113 or (ii) a notice of allowance under §1.311, whichever occurs first.

Accompanying this Information Disclosure Statement is the fee set forth in 37 C.F.R. §1.17(p).

The Commissioner is authorized to charge the $180.00 fee in payment of the Information Disclosure Statement Fee to Deposit Account No. 504480. If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 504480 (Order No. IGT1P096/P000824-001).

Respectfully submitted,
Weaver Austin Villeneuve & Sampson LLP

/Reginald J. Suyat/

Reginald J. Suyat
Registration No. 28,172

Nishitkumar V. Patel
Reg. No. 65,546

P.O. Box 70250
Oakland, CA 94612-0250
(510) 663-1100

Form 1449 (Modified)	Atty Docket No.	IGT1P096/P000824-001
	Application No.:	10/621,873
Information Disclosure	Applicant	Richard L. Wilder et al.
Statement By Applicant	Filing Date	July 16, 2003
	Group	3714
	Submitted	June 22, 2010
		Page 1 of 1

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class

Foreign Patent or Published Foreign Patent Application

Examiner Initial		Document No.	Publication Date	Country or Patent Office	Class	Sub-Class	Translation Yes	No
	1.	2404757	02/09/2005	GB				

Other Documents

Examiner Initial	No.	Author, Title, Place (e.g. Journal) of Publication, Date
	2.	Examination Report dated October 18, 2005 from Application No. GB0415710.3 [IGT1P096GB]

Examiner	Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

EXHIBIT B

United States
Patent and
Trademark Office

Deposit Account Statement

Requested Statement Month: June 2010
Deposit Account Number: 504480
Name: WEAVER AUSTIN VILLENEUVE AND SAMPSON, LLP
Attention: REID C. MILLER
Street Address 1: 500 12TH STREET, SUITE 200
Street Address 2:
City: OAKLAND
State: CA
Zip: 94607
Country: UNITED STATES

DATE SEQ	POSTING REF TXT	ATTORNEY DOCKET NBR	FEE CODE	AMT	BAL
06/24 1	10621873	IGT1P096/P000824-001	1202	$208.00	$25,148.00

Document code: WFEE

United States Patent and Trademark Office
Sales Receipt for Accounting Date: 06/24/2010

JHARRIS   SALE #00000001   Mailroom Dt: 06/22/2010   504480   10621873
                01   FC : 1202   208.00 DA

EXHIBIT C

PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

07/22/2003 DEHMANU1 00000080 10621873

01 FC:1001   750.00 OP
02 FC:1201   84.00 OP

PTO-1556
(5/87)

PATENT APPLICATION FEE DETERMINATION RECORD
Effective January 1, 2003

Application or Docket Number

10-621-873

CLAIMS AS FILED - PART I

	(Column 1)	(Column 2)
TOTAL CLAIMS	20	
FOR	NUMBER FILED	NUMBER EXTRA
TOTAL CHARGEABLE CLAIMS	20 minus 20=	* 0
INDEPENDENT CLAIMS	4 minus 3 =	* 1
MULTIPLE DEPENDENT CLAIM PRESENT		☐

* If the difference in column 1 is less than zero, enter "0" in column 2

SMALL ENTITY TYPE ☐ **OR OTHER THAN SMALL ENTITY**

RATE	FEE		RATE	FEE
BASIC FEE	375.00	OR	BASIC FEE	750.00
X$ 9=		OR	X$18=	
X42=		OR	X84=	84
+140=		OR	+280=	
TOTAL		OR	TOTAL	834

CLAIMS AS AMENDED - PART II

OTHER THAN SMALL ENTITY OR SMALL ENTITY

AMENDMENT A

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA
Total	*	Minus	**	=
Independent	*	Minus	***	=
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				☐

RATE	ADDI-TIONAL FEE		RATE	ADDI-TIONAL FEE
X$ 9=		OR	X$18=	
X42=		OR	X84=	
+140=		OR	+280=	
TOTAL ADDIT. FEE		OR	TOTAL ADDIT. FEE	

AMENDMENT B

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA
Total	*	Minus	**	=
Independent	*	Minus	***	=
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				☐

RATE	ADDI-TIONAL FEE		RATE	ADDI-TIONAL FEE
X$ 9=		OR	X$18=	
X42=		OR	X84=	
+140=		OR	+280=	
TOTAL ADDIT. FEE		OR	TOTAL ADDIT. FEE	

AMENDMENT C

	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA
Total	*	Minus	**	=
Independent	*	Minus	***	=
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM				☐

RATE	ADDI-TIONAL FEE		RATE	ADDI-TIONAL FEE
X$ 9=		OR	X$18=	
X42=		OR	X84=	
+140=		OR	+280=	
TOTAL ADDIT. FEE		OR	TOTAL ADDIT. FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20."
***If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3."
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

EXHIBIT D

Electronic Acknowledgement Receipt

EFS ID:	3976137
Application Number:	10621873
International Application Number:	
Confirmation Number:	1742
Title of Invention:	Secured verification of configuration data for field programmable gate array devices
First Named Inventor/Applicant Name:	Harold E. Mattice
Customer Number:	22434
Filer:	David P. Olynick/Emma Durrell
Filer Authorized By:	David P. Olynick
Attorney Docket Number:	IGT1P096/P-824
Receipt Date:	19-SEP-2008
Filing Date:	16-JUL-2003
Time Stamp:	18:29:53
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	$510
RAM confirmation Number	9436
Deposit Account	504480
Authorized User	
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	
Charge any Additional Fees required under 37 C.F.R. Section 1.21 (Miscellaneous fees and charges)	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	IGT1P096_Amendment_D_Transmittal.pdf	24369 / 71922602751be04c24d8dac1b0b8986192ecb80d	no	1

Warnings:

Information:

| 2 | Fee Worksheet (PTO-06) | fee-info.pdf | 32068 / 85ada7915e95c64b6a3e84ff0804b75af445a527 | no | 2 |

Warnings:

Information:

| Total Files Size (in bytes): | 56437 |

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

Electronic Patent Application Fee Transmittal

Application Number:	10621873
Filing Date:	16-Jul-2003
Title of Invention:	Secured verification of configuration data for field programmable gate array devices
First Named Inventor/Applicant Name:	Harold E. Mattice
Filer:	David P. Olynick/Emma Durrell
Attorney Docket Number:	IGT1P096/P-824

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Claims in excess of 20	1202	1	50	50
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Extension - 2 months with $0 paid	1252	1	460	460
Miscellaneous:				
Total in USD ($)				**510**

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 10/621,873	Filing Date 07/16/2003	☐ To be Mailed

APPLICATION AS FILED – PART I

OTHER THAN

	(Column 1)	(Column 2)	SMALL ENTITY ☐		OR	SMALL ENTITY	
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)		RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (l), or (m))	N/A	N/A	N/A			N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =		OR	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =			X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $250 ($125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	

APPLICATION AS AMENDED – PART II

OTHER THAN

		(Column 1)		(Column 2)	(Column 3)	SMALL ENTITY		OR	SMALL ENTITY	
AMENDMENT	09/19/2008	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)		RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 21	Minus	** 20	= 1	X $ =		OR	X $50=	50
	Independent (37 CFR 1.16(h))	* 4	Minus	***4	= 0	X $ =		OR	X $210=	0
	☐ Application Size Fee (37 CFR 1.16(s))									
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	50

		(Column 1)		(Column 2)	(Column 3)					
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)		RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =		OR	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =		OR	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))									
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.

** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".

*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".

The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

Legal Instrument Examiner:
/JAMES F. ELLIOTT/

EXHIBIT E

Electronic Acknowledgement Receipt

EFS ID:	6079643
Application Number:	10621873
International Application Number:	
Confirmation Number:	1742
Title of Invention:	Secured verification of configuration data for field programmable gate array devices
First Named Inventor/Applicant Name:	Harold E. Mattice
Customer Number:	79646
Filer:	David P. Olynick/emma durrell
Filer Authorized By:	David P. Olynick
Attorney Docket Number:	IGT1P096/P000824-001
Receipt Date:	15-SEP-2009
Filing Date:	16-JUL-2003
Time Stamp:	20:01:00
Application Type:	Utility under 35 USC 111(a)

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	$1356
RAM confirmation Number	5593
Deposit Account	504480
Authorized User	
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	
Charge any Additional Fees required under 37 C.F.R. Section 1.21 (Miscellaneous fees and charges)	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	IGT1P096_RCE_091509.pdf	122721 6368bb3d8395fbe9c3d5eaf9c426582e107ee24e	no	2

Warnings:

This is not a USPTO supplied RCE SB30 form.

Information:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
2		IGT1P096_Amendment_F.pdf	128389 63a32b1c916ad8b49ed0b8edf271d34be53337ff	yes	11

Multipart Description/PDF files in .zip description		
Document Description	Start	End
Amendment After Final	1	1
Claims	2	9
Applicant Arguments/Remarks Made in an Amendment	10	11

Warnings:

Information:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
3	Fee Worksheet (PTO-875)	fee-info.pdf	33842 c03ac3abb4eb7d40be30f3ed8cc35c63a1f8040e	no	2

Warnings:

Information:

Total Files Size (in bytes):	284952

This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.

New Applications Under 35 U.S.C. 111
If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371
If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office
If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.

Electronic Patent Application Fee Transmittal

Application Number:	10621873
Filing Date:	16-Jul-2003
Title of Invention:	Secured verification of configuration data for field programmable gate array devices
First Named Inventor/Applicant Name:	Harold E. Mattice
Filer:	David P. Olynick/emma durrell
Attorney Docket Number:	IGT1P096/P000824-001

Filed as Large Entity

Utility under 35 USC 111(a) Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Basic Filing:				
Pages:				
Claims:				
Claims in excess of 20	1202	8	52	416
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD($)
Extension - 1 month with $0 paid	1251	1	130	130
Miscellaneous:				
Request for continued examination	1801	1	810	810
			Total in USD ($)	**1356**

PTO/SB/06 (07-06)
Approved for use through 1/31/2007. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD
Substitute for Form PTO-875

Application or Docket Number	10/621,873
Filing Date	07/16/2003
	☐ To be Mailed

APPLICATION AS FILED – PART I

OTHER THAN

FOR	(Column 1) NUMBER FILED	(Column 2) NUMBER EXTRA	SMALL ENTITY ☐ RATE ($)	FEE ($)	OR	SMALL ENTITY RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (l), or (m))	N/A	N/A	N/A			N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	
TOTAL CLAIMS (37 CFR 1.16(i))	minus 20 =	*	X $ =		OR	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =			X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $250 ($125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	

APPLICATION AS AMENDED – PART II

OTHER THAN

AMENDMENT	09/15/2009	(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	SMALL ENTITY RATE ($)	ADDITIONAL FEE ($)	OR	SMALL ENTITY RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 29	Minus	** 21	= 8	X $ =		OR	X $52=	416
	Independent (37 CFR 1.16(h))	* 4	Minus	*** 4	= 0	X $ =		OR	X $220=	0
	☐ Application Size Fee (37 CFR 1.16(s))									
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	416

AMENDMENT		(Column 1) CLAIMS REMAINING AFTER AMENDMENT		(Column 2) HIGHEST NUMBER PREVIOUSLY PAID FOR	(Column 3) PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)	OR	RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =		OR	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =		OR	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))									
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

Legal Instrument Examiner:
/DENISE t. LILES/

EXHIBIT F

PTO/SB/06 (07-06)
Approved for use through 1/31/2007. OMB 0651-0032
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PATENT APPLICATION FEE DETERMINATION RECORD Substitute for Form PTO-875	Application or Docket Number 10/621,873	Filing Date 07/16/2003	☐ To be Mailed

APPLICATION AS FILED – PART I

	(Column 1)	(Column 2)	SMALL ENTITY ☐		OR	OTHER THAN SMALL ENTITY	
FOR	NUMBER FILED	NUMBER EXTRA	RATE ($)	FEE ($)		RATE ($)	FEE ($)
☐ BASIC FEE (37 CFR 1.16(a), (b), or (c))	N/A	N/A	N/A			N/A	
☐ SEARCH FEE (37 CFR 1.16(k), (i), or (m))	N/A	N/A	N/A			N/A	
☐ EXAMINATION FEE (37 CFR 1.16(o), (p), or (q))	N/A	N/A	N/A			N/A	
TOTAL CLAIMS (37 CFR 1.16(l))	minus 20 =	*	X $ =		OR	X $ =	
INDEPENDENT CLAIMS (37 CFR 1.16(h))	minus 3 =	*	X $ =			X $ =	
☐ APPLICATION SIZE FEE (37 CFR 1.16(s))	If the specification and drawings exceed 100 sheets of paper, the application size fee due is $250 ($125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).						
☐ MULTIPLE DEPENDENT CLAIM PRESENT (37 CFR 1.16(j))							
* If the difference in column 1 is less than zero, enter "0" in column 2.			TOTAL			TOTAL	

APPLICATION AS AMENDED – PART II

		(Column 1)		(Column 2)	(Column 3)	SMALL ENTITY		OR	OTHER THAN SMALL ENTITY	
AMENDMENT	06/22/2010	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)		RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	* 25	Minus	** 21	= 4	X $ =		OR	X $52=	208
	Independent (37 CFR 1.16(h))	* 4	Minus	*** 4	= 0	X $ =		OR	X $220=	0
	☐ Application Size Fee (37 CFR 1.16(s))							OR		
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	208

		(Column 1)		(Column 2)	(Column 3)					
AMENDMENT		CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE ($)	ADDITIONAL FEE ($)		RATE ($)	ADDITIONAL FEE ($)
	Total (37 CFR 1.16(i))	*	Minus	**	=	X $ =		OR	X $ =	
	Independent (37 CFR 1.16(h))	*	Minus	***	=	X $ =		OR	X $ =	
	☐ Application Size Fee (37 CFR 1.16(s))							OR		
	☐ FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM (37 CFR 1.16(j))							OR		
						TOTAL ADD'L FEE		OR	TOTAL ADD'L FEE	

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20".
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3".
The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

Legal Instrument Examiner:
/JAMILAH Z. HARRIS/